Amendment and Response to Office Action 10/630,994 (Docket No.: 200311225-1)

February 16, 2006

Page 6 of 15

AMENDMENTS TO THE SPECIFICATION

Please replace the title of the application with the following title, where the following title is marked up to show changes relative to the original title.

Persistant Volatile Memory Fault Tracking <u>Using Entries in the Non-Volatile</u> <u>Memory of a Fault Storage Unit</u>

Please also replace the paragraph starting on page 7, line 15 of the application with the following paragraph, where the following paragraph is marked up to show changes made relative to the original paragraph.

In one embodiment, a manageability processor (MP) 420 may be included on the system board 400. The MP 420 may be used to control the monitoring of status, the logging of events and abnormal conditions, and the recovery capabilities in relation to the memory system. In one embodiment, the manageability processor 420 may be configured to be compliant with the intelligent platform management interface (IPMI) specification. The IPMI specification is currently available from Intel via the Internet by way of the following URL: http://www.intel.com/design/servers/ipmi/spec.htm. The Intelligent Management Platform provides functions that are available independent of the main processors, BIOS, and operating system. IPMI messaging uses a request/response protocol. IPMI request messages (also called commands) are grouped into functional command sets, using a field called the network function code. There are command sets for sensor and event related commands, chassis commands, and so on.